

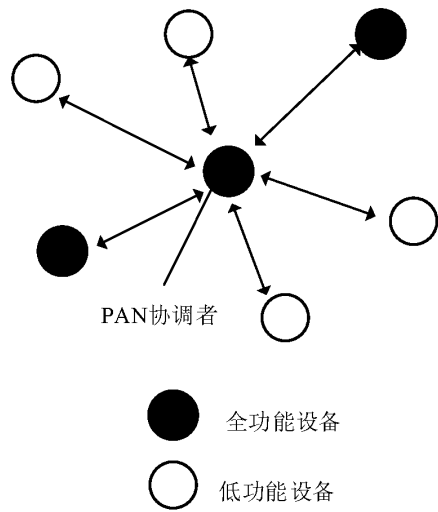


图 1

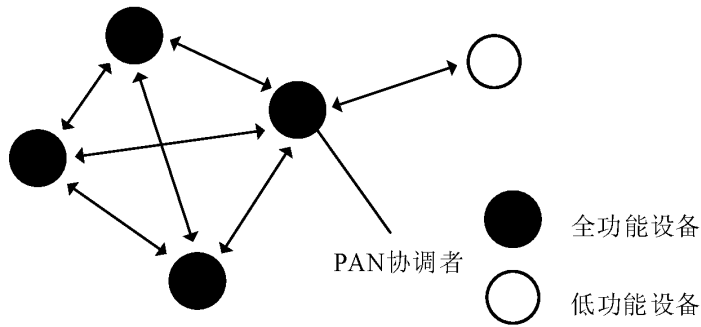


图 2

$$H = \begin{matrix} & \begin{matrix} V_1 & V_2 & V_3 & V_4 & V_5 & V_6 & V_7 & V_8 \end{matrix} \\ \begin{bmatrix} 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 \\ 1 & 0 & 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 & 0 & 0 & 1 \end{bmatrix} & \begin{matrix} C_1 \\ C_2 \\ C_3 \\ C_4 \end{matrix} \end{matrix}$$

图 3a

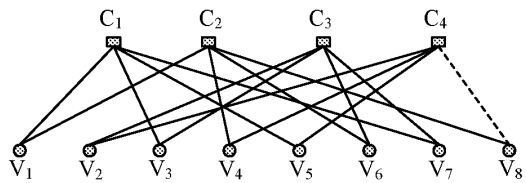


图 3b

WO 2024/002010												PCT/CN2023/102418											
0	-	-	-	0	0	-	-	0	-	-	0	1	0	-	-	-	-	-	-	-	-	-	-
22	0	-	-	17	-	0	0	12	-	-	-	-	0	0	-	-	-	-	-	-	-	-	-
6	-	0	-	10	-	-	-	24	-	0	-	-	-	0	0	-	-	-	-	-	-	-	-
2	-	-	0	20	-	-	-	25	0	-	-	-	-	-	0	0	-	-	-	-	-	-	-
23	-	-	-	3	-	-	-	0	-	9	11	-	-	-	-	0	0	-	-	-	-	-	-
24	-	23	1	17	-	3	-	10	-	-	-	-	-	-	-	0	0	-	-	-	-	-	-
25	-	-	-	8	-	-	-	7	18	-	-	0	-	-	-	-	0	0	-	-	-	-	-
13	24	-	-	0	-	8	-	6	-	-	-	-	-	-	-	-	0	0	-	-	-	-	-
7	20	-	16	22	10	-	-	23	-	-	-	-	-	-	-	-	-	0	0	-	-	-	-
11	-	-	-	19	-	-	-	13	-	3	17	-	-	-	-	-	-	-	0	0	-	-	-
25	-	8	-	23	18	-	14	9	-	-	-	-	-	-	-	-	-	-	-	0	0	-	-
3	-	-	-	16	-	-	2	25	5	-	-	1	-	-	-	-	-	-	-	-	-	-	0

图 4a

$$\begin{bmatrix} 0 & 1 & 0 & \dots & 0 \\ 0 & 0 & 1 & \dots & 1 \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ 0 & 0 & 0 & \dots & 0 \\ 1 & 0 & 0 & \dots & 1 \end{bmatrix}$$

图 4b

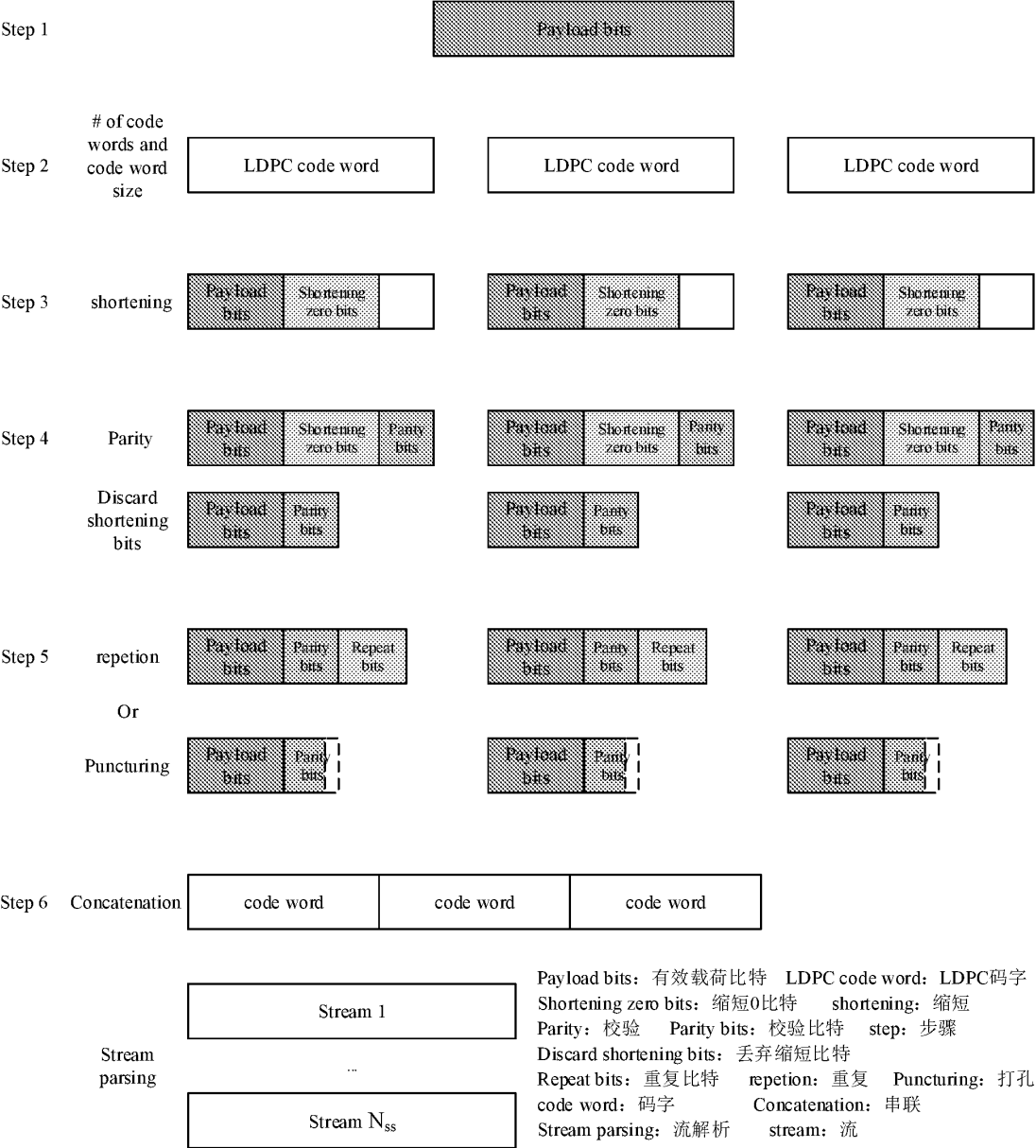


图 5

40	-	-	-	22	-	49	23	43	-	-	-	1	0	-	-	-	-	-	-	-	-	-	-
50	1	-	-	48	35	-	-	13	-	30	-	-	0	0	-	-	-	-	-	-	-	-	-
39	50	-	-	4	-	2	-	-	-	49	-	-	0	0	-	-	-	-	-	-	-	-	-
33	-	-	38	37	-	-	4	1	-	-	-	-	-	0	0	-	-	-	-	-	-	-	-
45	-	-	-	0	22	-	-	20	42	-	-	-	-	-	0	0	-	-	-	-	-	-	-
51	-	-	48	35	-	-	-	44	-	18	-	-	-	-	-	0	0	-	-	-	-	-	-
47	11	-	-	-	17	-	-	51	-	-	-	0	-	-	-	-	0	0	-	-	-	-	-
5	-	25	-	6	-	45	-	13	40	-	-	-	-	-	-	-	0	0	-	-	-	-	-
33	-	-	34	24	-	-	-	23	-	46	-	-	-	-	-	-	-	0	0	-	-	-	-
1	-	27	-	1	-	-	-	38	-	44	-	-	-	-	-	-	-	-	0	0	-	-	-
-	18	-	-	23	-	-	8	0	35	-	-	-	-	-	-	-	-	-	-	0	0	-	-
49	-	17	-	30	-	-	-	34	-	19	1	-	-	-	-	-	-	-	-	-	-	0	-

图 6a

WO 2024/002010												PCT/CN2023/102418											
57	-	-	-	50	-	11	-	50	-	79	-	1	0	-	-	-	-	-	-	-	-	-	-
3	-	28	-	0	-	-	-	55	7	-	-	-	0	0	-	-	-	-	-	-	-	-	-
30	-	-	-	24	37	-	-	56	14	-	-	-	-	0	0	-	-	-	-	-	-	-	-
62	53	-	-	53	-	-	3	35	-	-	-	-	-	0	0	-	-	-	-	-	-	-	-
40	-	-	20	66	-	-	22	28	-	-	-	-	-	-	0	0	-	-	-	-	-	-	-
0	-	-	-	8	-	42	-	50	-	-	8	-	-	-	-	0	0	-	-	-	-	-	-
69	79	79	-	-	-	56	-	52	-	-	0	-	-	-	-	-	0	0	-	-	-	-	-
65	-	-	-	38	57	-	-	72	-	27	-	-	-	-	-	-	-	0	0	-	-	-	-
64	-	-	-	14	52	-	-	30	-	-	32	-	-	-	-	-	-	-	0	0	-	-	-
-	45	-	70	0	-	-	-	77	9	-	-	-	-	-	-	-	-	-	-	0	0	-	-
2	56	-	57	35	-	-	-	-	12	-	-	-	-	-	-	-	-	-	-	-	0	0	-
24	-	61	-	60	-	-	27	51	-	-	16	1	-	-	-	-	-	-	-	-	-	0	0

图 6b

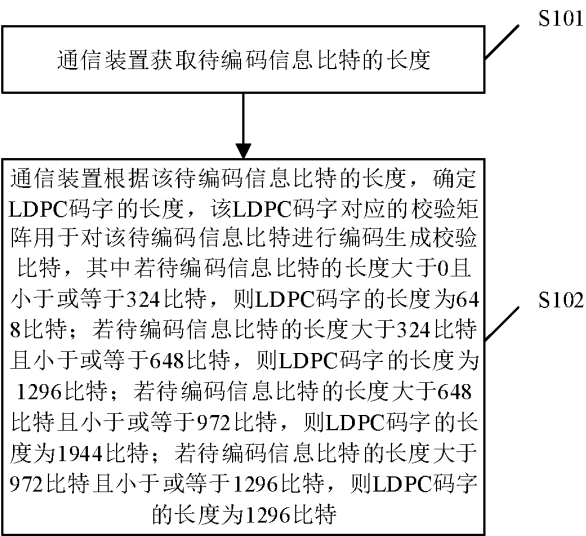
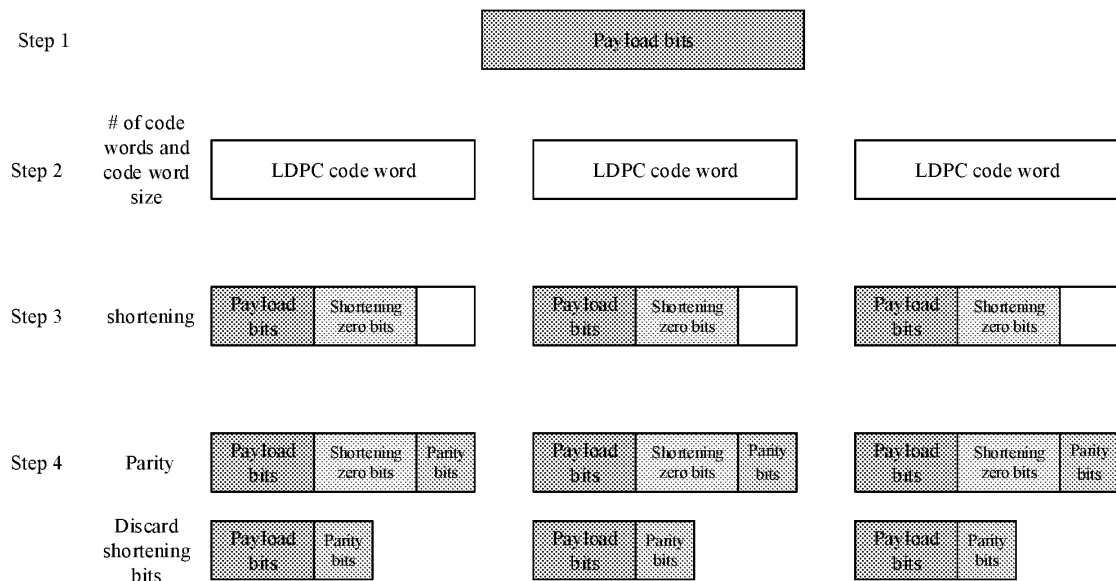


图 7



Payload bits: 有效载荷比特 LDPC code word: LDPC码字
 Shortening zero bits: 缩短0比特 shortening: 缩短
 Parity: 校验 Parity bits: 校验比特 step: 步骤
 Discard shortening bits: 丢弃缩短比特

图 8

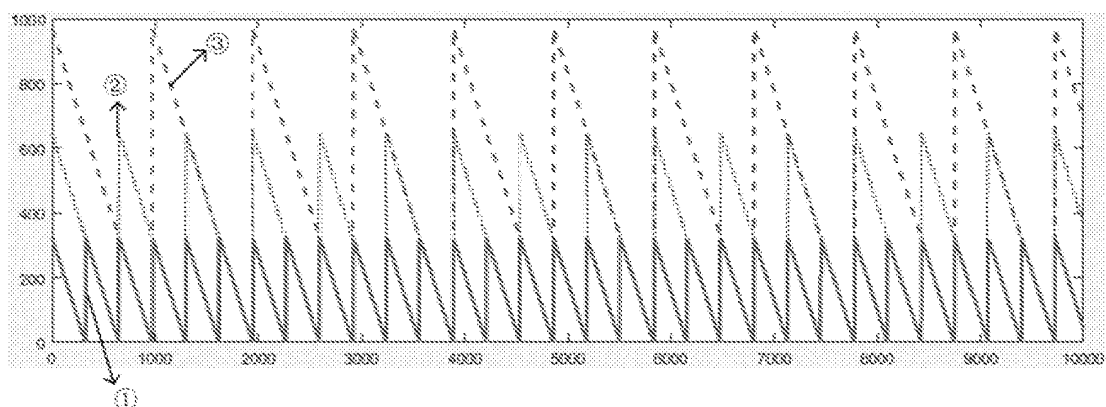


图 9

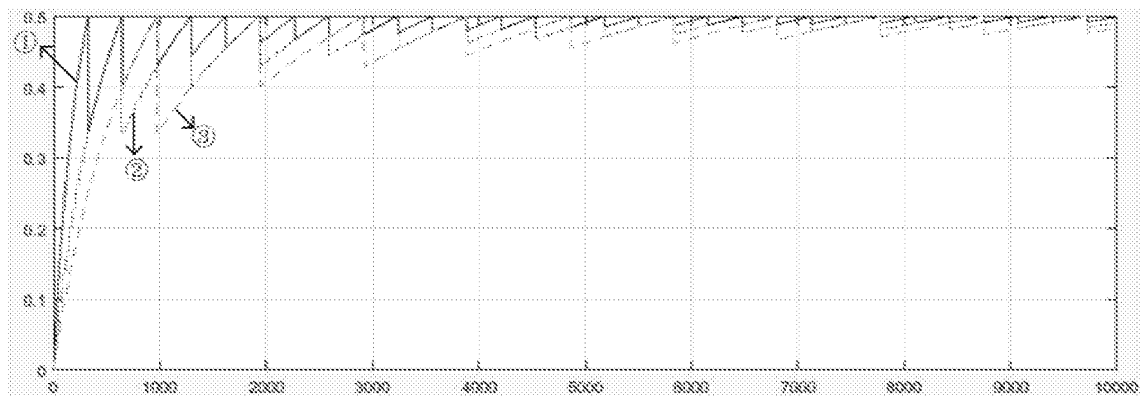


图 10

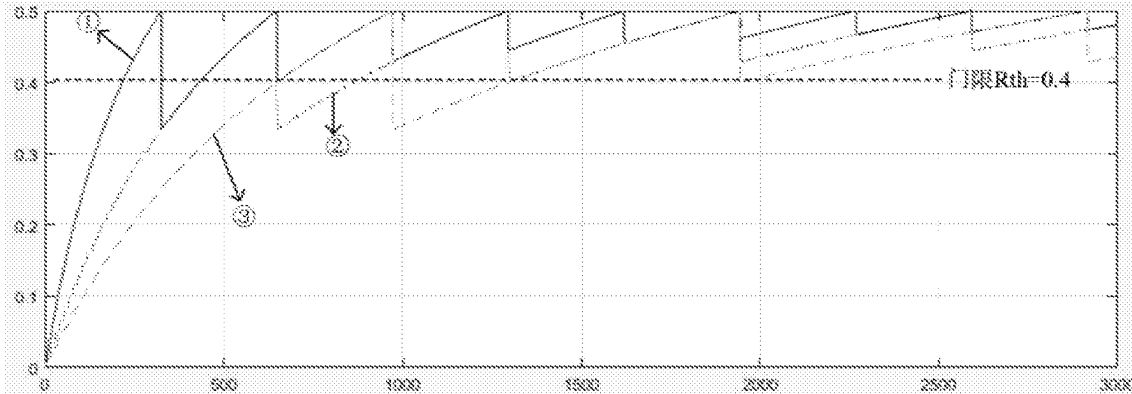


图 11a

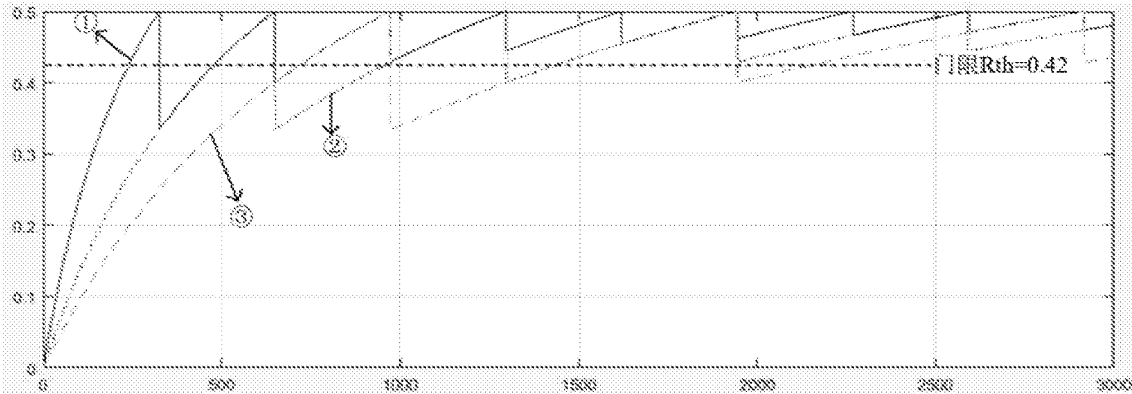


图 11b

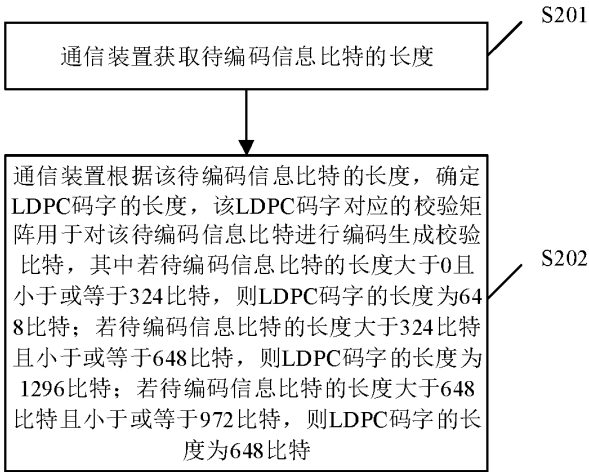


图 12

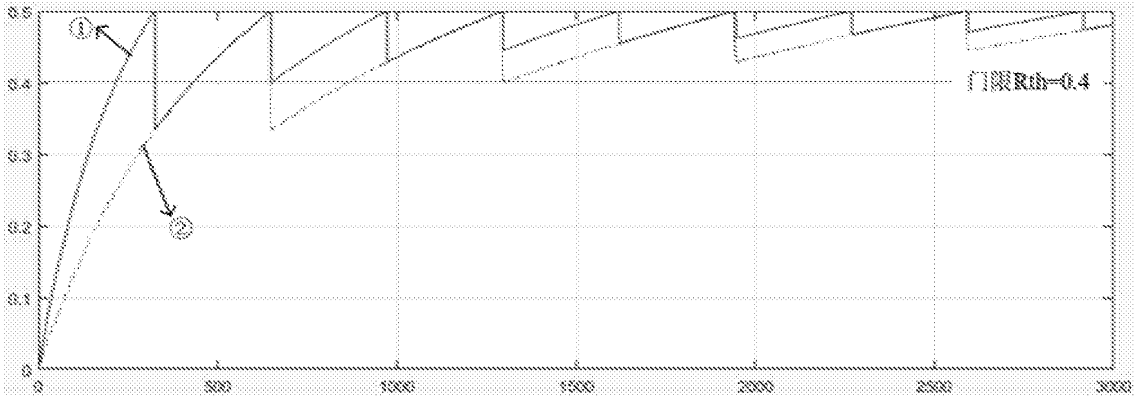


图 13a

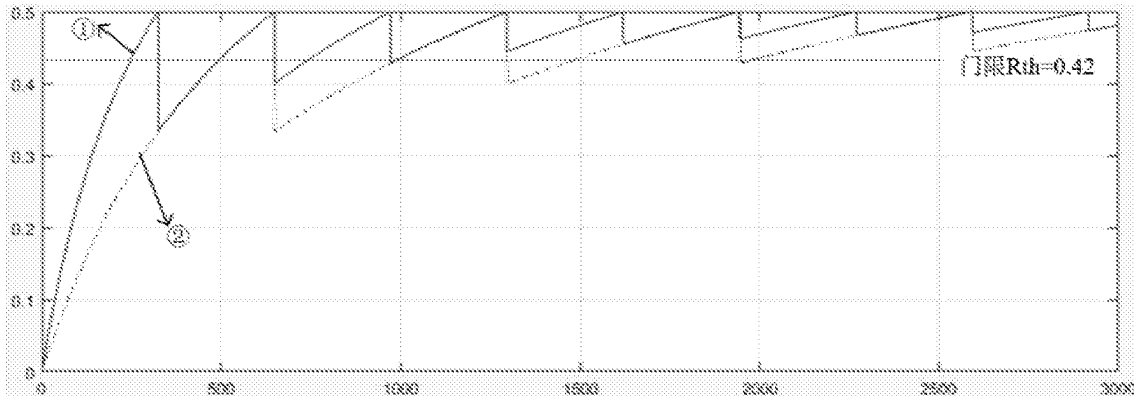


图 13b

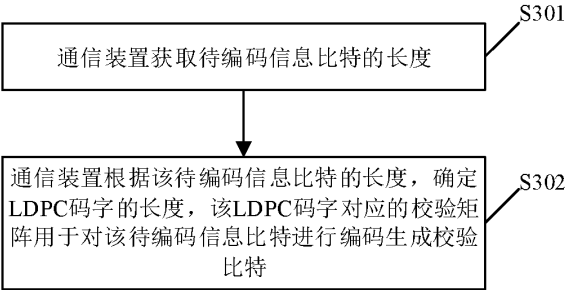


图 14

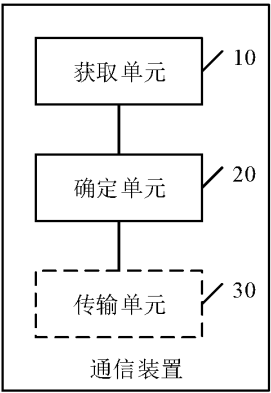


图 15

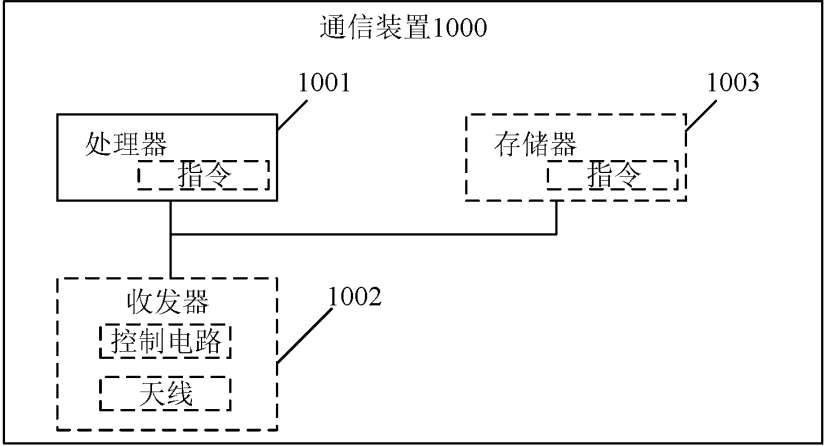


图 16

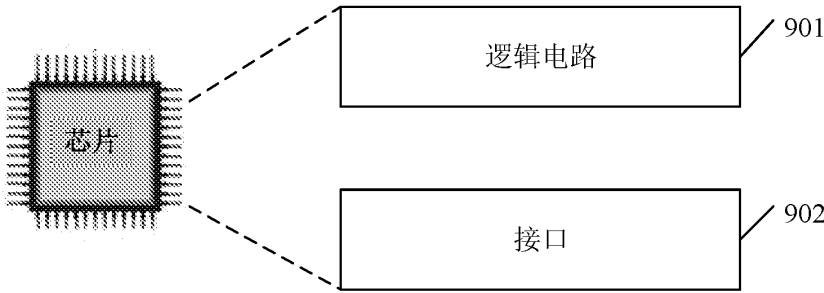


图 17